Fig.1

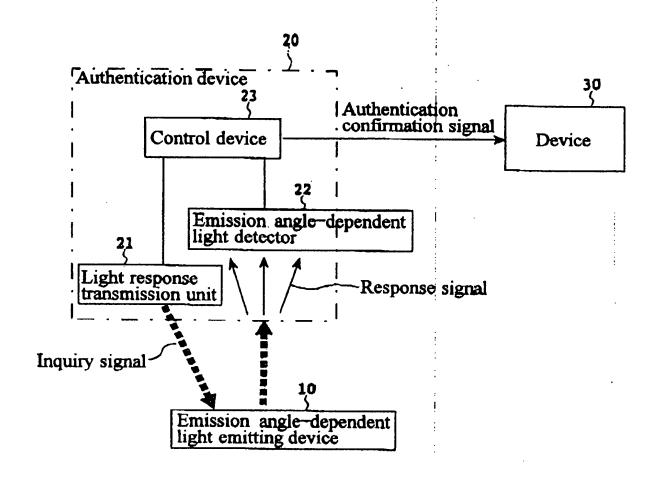


Fig.2

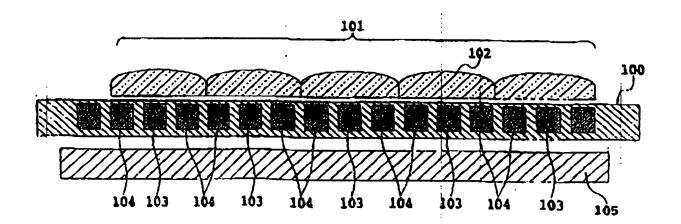


Fig.3

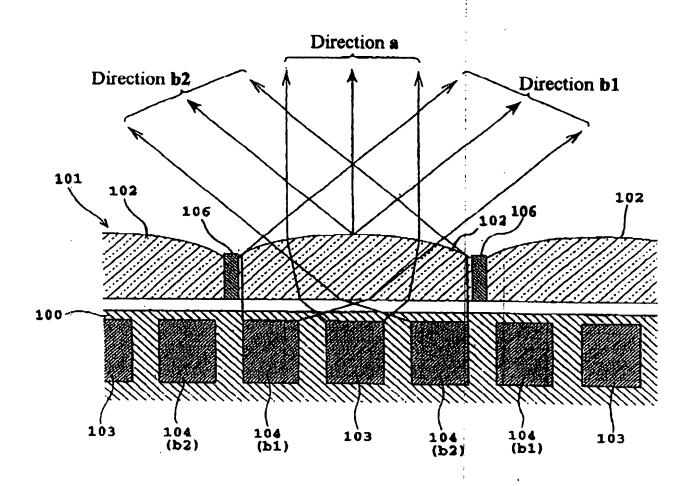


Fig.4

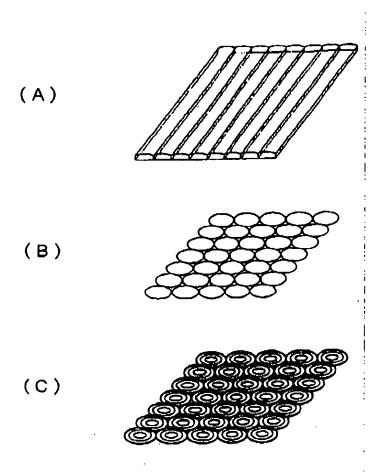


Fig.5

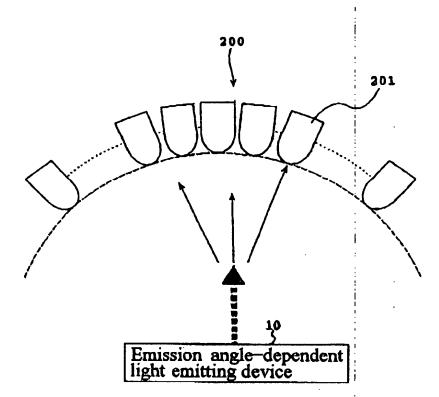


Fig.6

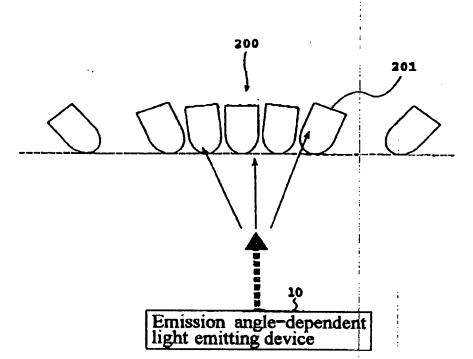
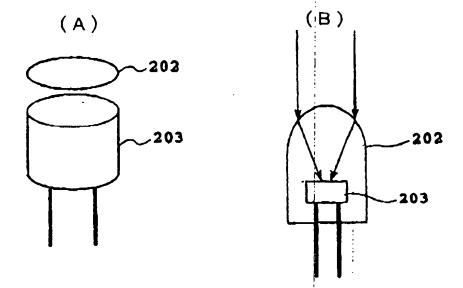


Fig.7



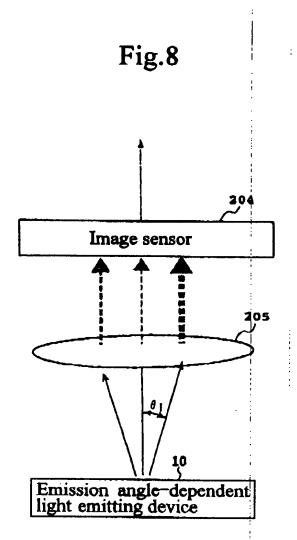


Fig.9

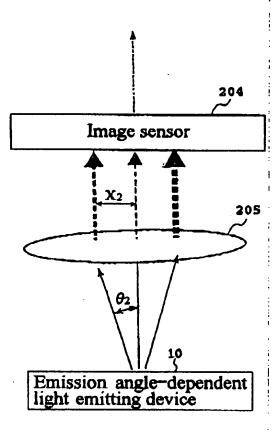


Fig.10

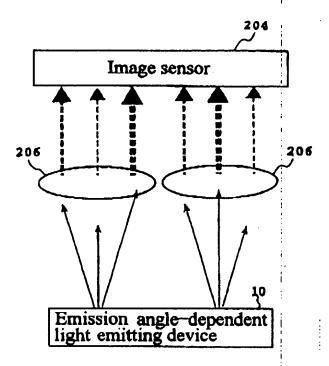


Fig.11

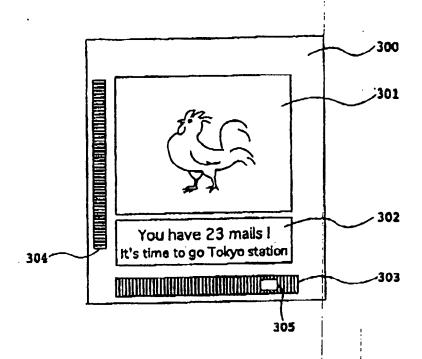


Fig.12 Photoreceiving circuit 400 Key-input circuit 404 407 Display memory Liquid crystal control integrated circuit 401 \{ LED output circuit 403 (Liquid crystal operating gate driver 402 **Processor** Computation memory Liquid crystal panel

Fig.13

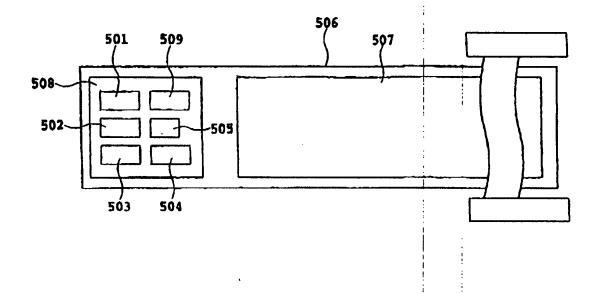


Fig.14

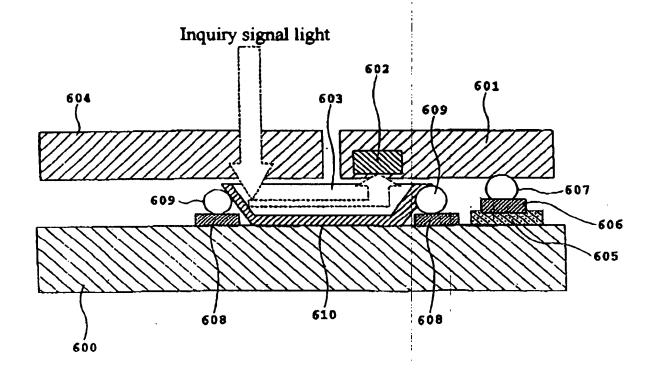


Fig.15

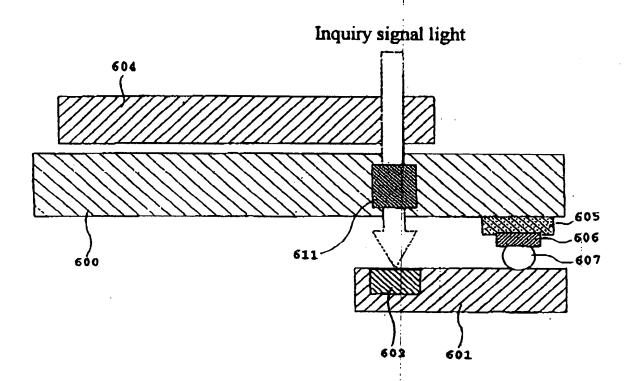


Fig.16

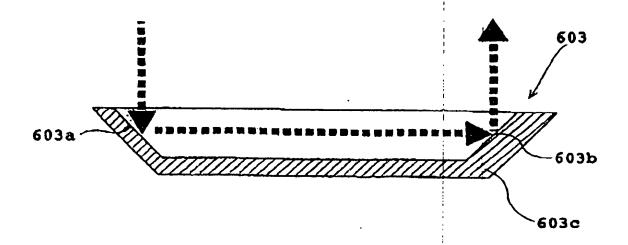


Fig.17

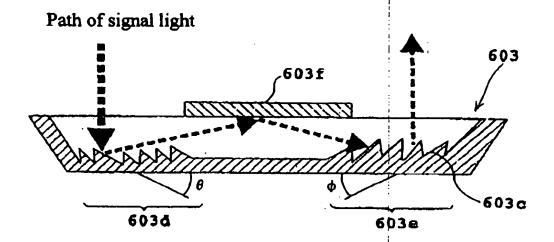


Fig.18

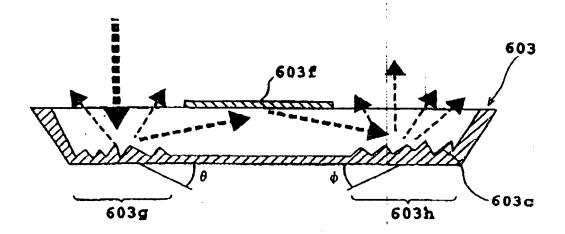


Fig.19

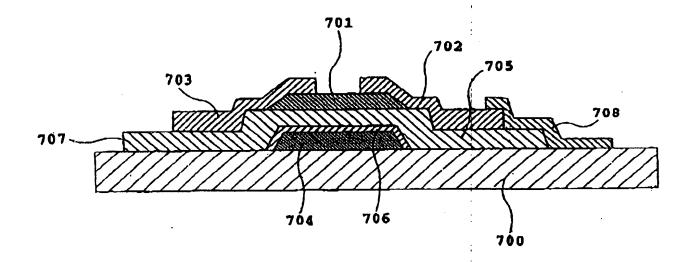


Fig.20

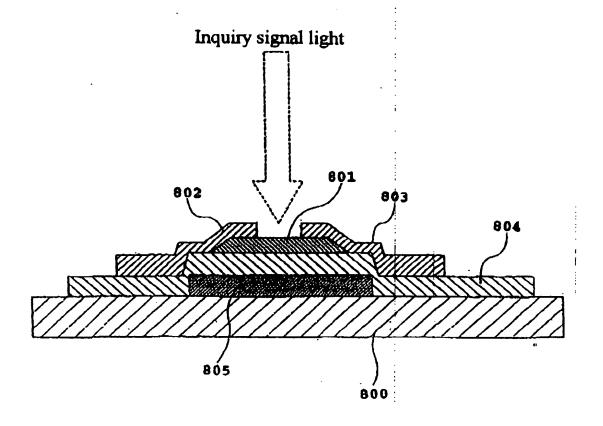


Fig.21

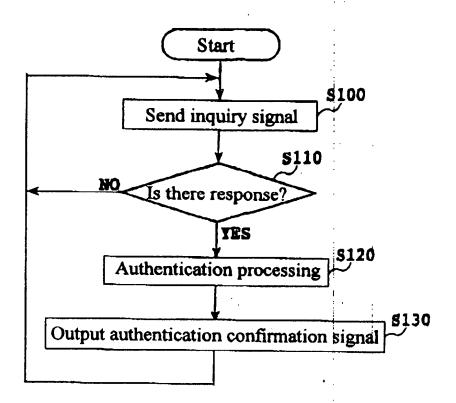
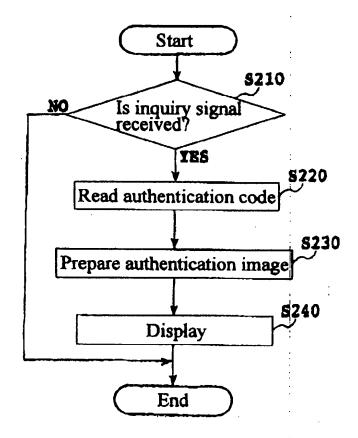
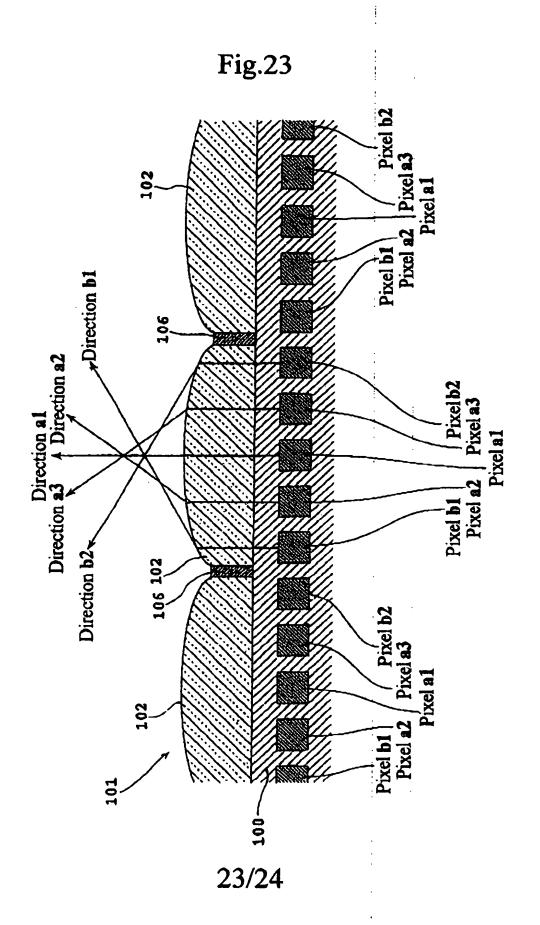
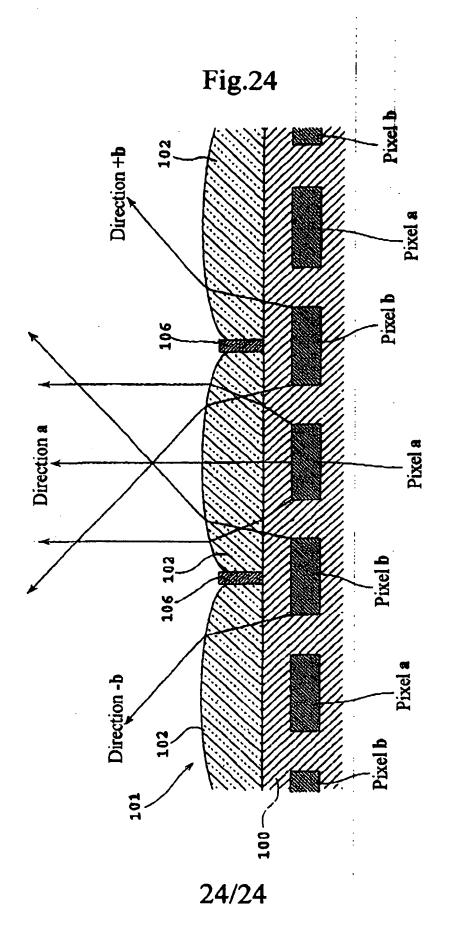


Fig.22







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